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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Chan et al.

Serial No.: 10/051,562

Group Art Unit: 2822

Filed: January 18, 2002

Examiner: Novacek, Christy L.

For: SELF-ALIGNED DOUBLE-GATE MOSFET BY SELECTIVE EPITAXY
AND SILICON WAFER BONDING TECHNIQUES

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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SEP 25 2003

AMENDMENT UNDER 37 C.F.R. §1.116

OFFICIAL

Sir:

In response to the Office Action mailed July 23, 2003, please amend the above-identified patent application as follows:

IN THE CLAIMS:

Please cancel claim 36 and amend the remaining claims as follows.

09/23/2003 AFORD1 00000001 500510 10051562

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